

Web Images Maps News Shopping Gmail more ▾

Sign in



a high performance microarchitecture iwth hardware programmable functio

Search

Web

Results 1 - 3 of 3 for a high performance microarchitecture iwth hardware programmable functional units

Did you mean: a high performance microarchitecture *with* hardware programmable functional units

The Encyclopedia of Computer Languages

SmallScript: A user programmable framework based on Smalltalk and Postscript. in

ACM 1998 workshop on Java for high-performance network computing. ...

nopl.murdoch.edu.au/locatesource.prx?txtfile=da - 827k - [Cached](#) - [Similar pages](#)

A High-Performance Microarchitecture with Hardware-Programmable ...

Razdan, R. Smith, M.D. "A High-Performance Microarchitecture with Hardware-

Programmable Functional Units", MICRO-27-11/94 San Jose, ...

citeseer.ist.psu.edu/razdan94highperformance.html - 26k - [Cached](#) - [Similar pages](#)

by R Razdan - 1994 - Cited by 274 - Related articles - All 20 versions

A High-Performance Microarchitecture With Hardware-Programmable ...

High-Performance Microarchitecture. with Hardware-Programmable Functional Units.

Rahul Razdan" and Michael D. Smith". Harvard University, Cambridge, MA 021 ...

ieeexplore.ieee.org/iel4/5811/15496/00717456.pdf - [Similar pages](#)

Did you mean to search for: a high performance microarchitecture *with* hardware programmable functional units

a high performance microarchitecture iwth hardware programmable functio

Search

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#) |
[Try Google Experimental](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)

[Web](#) [Images](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▾](#)[Sign in](#)

Google

dsp

Search

[Advanced Search](#)
[Preferences](#)[Web](#) [Groups](#)

Results 1 - 10 of about 46,300,000 for dsp. (0.12 seconds)

[Digital signal processing - Wikipedia, the free encyclopedia](#)

Digital signal processing (DSP) is concerned with the representation of the signals by a sequence of numbers or symbols and the processing of these signals. ...

en.wikipedia.org/wiki/Digital_signal_processing - 56k - [Cached](#) - [Similar pages](#)[Digital signal processor - Wikipedia, the free encyclopedia](#)

A digital signal processor (DSP or DSP micro) is a specialized microprocessor designed specifically for digital signal processing, generally in real-time ...

en.wikipedia.org/wiki/Digital_signal_processor - 61k - [Cached](#) - [Similar pages](#)[More results from en.wikipedia.org »](#)[The Scientist and Engineer's Guide to Digital Signal Processing](#)

A textbook of DSP techniques by Steven W. Smith, 1997, 640 pages. You can download the book for free. Clear explanation and low math.

www.dspguide.com/ - 10k - [Cached](#) - [Similar pages](#)[DSP Group, Inc.- Enabling DSP Applications and Solutions](#)

Supplier of speech recognition hardware for telephone applications.

[Show stock quote for DSPG](#)www.dspg.com/ - 21k - [Cached](#) - [Similar pages](#)[Digital Signal Processing \(DSP\) Development Tools | Texas Instruments](#)

Digital Signal Processing Development Tools (DSP) – Digital Signal Processing (DSP) offers technologies for innovative system development and application ...

focus.ti.com/dsp/docs/dsphome.tsp?sectionId=46 - 203k - [Cached](#) - [Similar pages](#)[Nothing But Net](#)

The premium Internet Service Provider in Northern California providing email, web hosting, and superior Internet access. Excellent technical support and ...

www.dsp.net/ - 30k - [Cached](#) - [Similar pages](#)[Defense Standardization Program](#)

Department of Defense, Defense Standardization Program Office. DSP Logo. Contact Us | FAQs Search:. What's New About Us Policy Programs Chartered JSBs ...

www.dsp.dia.mil/ - 25k - [Cached](#) - [Similar pages](#)[dspGuru: DSP Central](#)

A center for DSP information exchange, to promote resource-sharing among DSP designers.

www.dspguru.com/ - 2k - [Cached](#) - [Similar pages](#)[Digital Signal Processing at Rice](#)

Includes research project information, publications archive, software and teaching tools and general DSP reference material.

[Show map of 6100 Main St, Houston, TX 77005](#)dsp.rice.edu/ - 5k - [Cached](#) - [Similar pages](#)[Bores Signal Processing - Introduction to DSP - index](#)

Training in DSP, software architectures and Philips TriMedia architectures.

www.bores.com/courses/intro/ - 5k - [Cached](#) - [Similar pages](#)

Searches related to: dsp

[dsp.oem](#)[dsp entertainment](#)[dsp jobs](#)[delta sigma pi](#)[defense support program](#)[direct stock purchase](#)[digital scrapbook place](#)[demokratik sol pa](#)[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#)[Next](#)

dsp

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#) |
[Try Google Experimental](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)

[Web](#) [Images](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▾](#)
[Sign in](#)

garp a mips processor with a reconfigurable coprocessor

[Search](#)[Advanced
Preferences](#)

Web

Results 1 - 10 of about 4,210 for [garp a mips processor with a reconfigurable coprocessor](#). (0.26 seconds)**[Welcome to IEEE Xplore 2.0: Garp: a MIPS processor with a ...](#)**

Garp: a MIPS processor with a reconfigurable coprocessor ... The Garp Architecture combines reconfigurable hardware with a standard MIPS processor on the ...
[ieeexplore.ieee.org/xpls/abs_all.jsp?tp=&number=624600](#) - [Similar pages](#)

[Garp: A Mips Processor with a Reconfigurable Coprocessor - Field ...](#)

Garp: A MIPS Processor with a Reconfigurable Coprocessor. John. R. Hauser and John Wawrzynek. University of California, Berkeley. Abstract ...
[ieeexplore.ieee.org/iel3/4927/13597/00624600.pdf?tp=&isnumber=624600](#) - [Similar pages](#)

[More results from \[ieeexplore.ieee.org\]\(#\) »](#)**[Garp: A MIPS Processor with a Reconfigurable Coprocessor - CiteSeerX](#)**

Document details from CiteSeerX (Isaac Council, Lee Giles): Typical reconfigurable machines exhibit shortcomings that make them less than ideal for ...
[citeseer.ist.psu.edu/hauser97garp.html](#) - 21k - [Cached](#) - [Similar pages](#)
 by J.R. Hauser - 1997 - [Cited by 625](#) - [Related articles](#) - [All 27 versions](#)

[garp: Berkeley Reconfigurable Architectures, Systems, and Software](#)

"Garp: A MIPS Processor with a Reconfigurable Coprocessor," by John R. Hauser and John Wawrzynek, published in Proceedings of the IEEE Symposium on ...
[brass.cs.berkeley.edu/garp.html](#) - 6k - [Cached](#) - [Similar pages](#)

[Garp: a MIPS processor with a reconfigurable coprocessor](#)

Typical reconfigurable machines exhibit shortcomings that make them less than ideal for general-purpose computing. The Garp Architecture combines ...
[csdl.computer.org/comp/proceedings/fccm/1997/8159/00/8159G012abs.htm](#) - [Similar pages](#)
 by J.R. Hauser - [Cited by 625](#) - [Related articles](#) - [All 27 versions](#)

[Article: Garp: A MIPS Processor with a Reconfigurable Coprocessor](#)

The Garp Architecture combines reconfigurable hardware with a standard MIPS processor on the same die to retain the better features of both. ...
[www.jhauser.us/publications/1997_Hauser_Garp.html](#) - 4k - [Cached](#) - [Similar pages](#)

[Garp: A MIPS Processor with a Reconfigurable Coprocessor - KFUPM ...](#)

Hauser, John R. and Wawrzynek, John (1997) Garp: A MIPS Processor with a Reconfigurable Coprocessor. In IEEE Symposium on FPGAs for Custom Computing ...
[https://eprints.kfupm.edu.sa/42409/](#) - 15k - [Cached](#) - [Similar pages](#)

[Scientific Commons: Garp: A MIPS Processor with a Reconfigurable ...](#)

Keywords, John R. Hauser, John Wawrzynek Garp: A MIPS Processor with a Reconfigurable Coprocessor. Language, English. Relation, oai:CiteSeerPSU:461374, ...
[en.scientificcommons.org/270846](#) - 16k - [Cached](#) - [Similar pages](#)

[CUBA](#)

Garp: A MIPS processor with a reconfigurable coprocessor. In FCCM, pages 296-- 299, 1997. 10. M. Hummel, M. Krause, and D. O'Flaherty. ...
[portal.acm.org/citation.cfm?id=1375527.1375571&coll=portal&dl=ACM&CFID=15151515&CFTOKEN=6184618](#) - [Similar pages](#)

Garp: A MIPS Processor with a Reconfigurable Coprocessor ...

Scientific documents that cite the following paper: Garp: A MIPS Processor with a Reconfigurable Coprocessor, by JR Hauser, J Wawrzynek.

serv1.ist.psu.edu:8080/showciting.jsessionid=1148452FBD8F9F82D82037D4AFA50ECD?
cid=... - 41k - Cached - Similar pages

1 2 3 4 5 6 7 8 9 10 [Next](#)

garp a mips processor with a reconfigurable coprocessor

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied?](#) [Help us improve](#) |
[Try Google Experimental](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)